

National Semiconductor Translation and Innovation Centre (R&D Fab)

What is it?

The Agency for Science, Technology and Research (A*STAR) will invest about \$500 million to establish a new National Semiconductor Translation and Innovation Centre (NSTIC) R&D fabrication facility (R&D Fab) at JTC nanoSpace @ Tampines. This will be a national facility designed to drive advanced semiconductor research and innovation, with an initial focus on advanced packaging technologies, which are innovative methods of combining different semiconductor components into a single unit to make chips faster, more powerful and more efficient. The new facility aims to strengthen the local semiconductor ecosystem and bring technologies closer to commercialisation, by fostering partnerships with industry partners and academia. NSTIC (R&D Fab) at nanoSpace will commence operations in 2027.

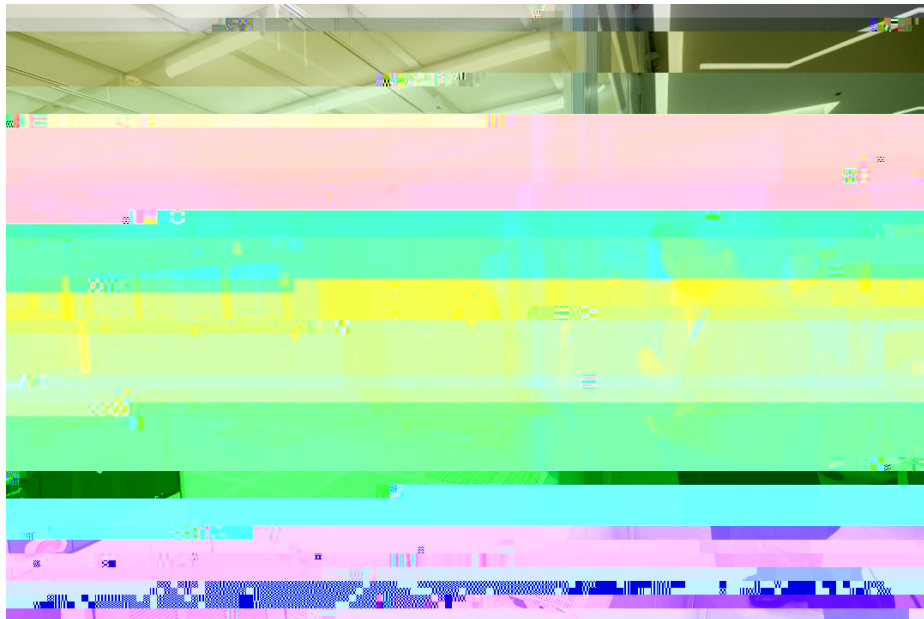
This is the next phase of the NSTIC programme, which was announced in MTI's Committee of Supply (COS) 2024. Then, it was announced that A*STAR would establish a national-level R&D translational platform for flat optics and silicon photonics, at a budget of about \$180 million. NSTIC commenced operations at Fusionopolis in April 2024. A*STAR has since broadened the Centre to cover R&D programmes and infrastructure for various key semiconductor technologies, with the aim of fostering ecosystem collaboration and boosting R&D translation outcomes for the semiconductor industry.

How does it work?

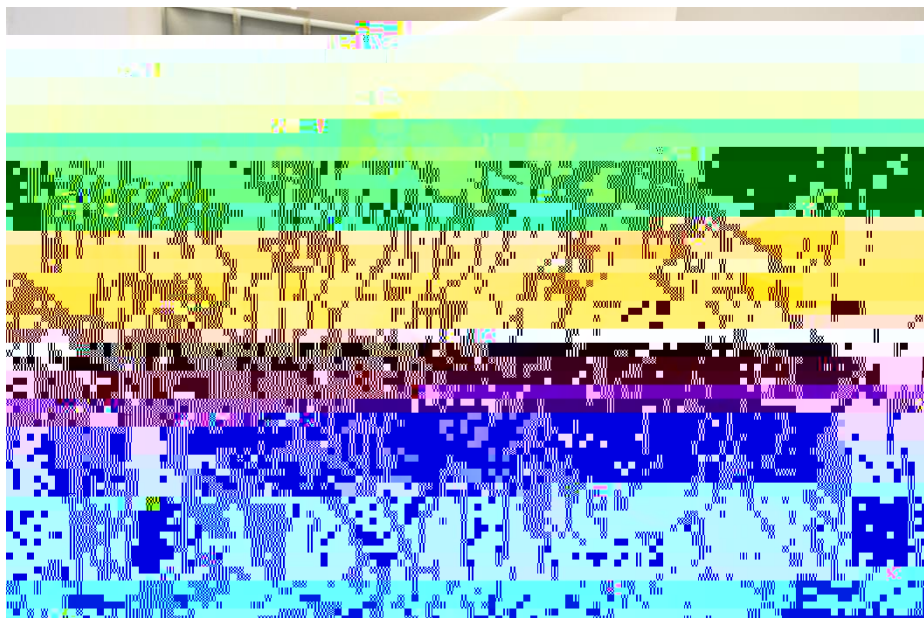
The NSTIC (R&D Fab) will house 12-inch (300mm) industry-grade tools that can enable faster production and cost savings, and will be used as a shared infrastructure resource, complemented by fab operations and research expertise. This will be particularly beneficial to SMEs and startups, as it provides them with access to critical semiconductor infrastructure which will otherwise require substantial upfront investments. NSTIC (R&D Fab) will operate as a collaborative hub where public and private sector players share access to state-of-the-art cleanroom facilities, tools, and fabrication operations, providing the opportunity to bridge the gap for lab-to-fab semiconductor R&D translation. Startups, SMEs, MNCs, and public sector research organisations can collaborate within the NSTIC (R&D Fab) ecosystem to carry out translational R&D. Synergies will arise as the NSTIC (R&D Fab)'s partners complement each other's capabilities, accelerating technology development and

Accessing Infrastructure and Tools

Annex: Photos of Deputy Prime Minister and Minister for Trade and Industry, Mr Gan Kim Yong's visit to the National Semiconductor Translation and Innovation Centre (NSTIC) at Fusionopolis



DPM Gan Kim Yong viewing the cleanroom where NSTIC, announced at MTI COS 2024, conducts R&D activities using shared cleanroom facilities at Fusionopolis. NSTIC (R&D Fab) at JTC nanoSpace @ Tampines will also provide shared cleanroom facilities.



MetaOptics Technologies showcasing its product, which leverages flat optics, to DPM Gan Kim Yong.